

IPW
PATENT APPLICATION

THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Myoung-soo KIM. **DOCKET NO.** 8836-176 (IB12207-US)
SERIAL NO: 10/722,935 **GROUP:** Art Unit 2811
FILED: November 26, 2003 **EXAMINER:** Vu, Hung K.
FOR: **SEMICONDUCTOR DEVICE HAVING ONE-TIME
PROGRAMMABLE ROM AND METHOD OF FABRICATING
THE SAME**

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Office Action dated July 22, 2004, applicant provisionally elects the claims of Group I (claim nos. 1-13), with traverse. While the semiconductor device of Group I and the method of making a semiconductor device Group II may be distinct for the reasons set forth by the Examiner, applicant believes that simultaneous examination will not present an undue burden. For example, the claims of Group I are drawn to a semiconductor device with an OTP ROM form over a substrate and the claims of Group II are drawn to a method of fabricating such semiconductor device. The two Groups of claims include subject matters that are classified commonly in class 438.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Mail Stop Amendment, Commissioner for Patents, PO Box 1450, Alexandria, VA22313-1450 on August 18, 2004

Dated: _____

8/18/04




Frank Chau

Under such circumstances, the Examiner is encouraged to maintain all claims in the same application. See, MPEP § 803.

Early and favorable consideration of this application are earnestly solicited.

Respectfully submitted,

By:



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